

D.J.
#2 10-1701
IDS 4/1/01
NIT-292

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

H. FUJII et al

Serial No.

Filed: August 29, 2001

For: PROCESSOR SYSTEM INCLUDING DYNAMIC TRANSLATION FACILITY, BINARY TRANSLATION PROGRAM, THAT RUNS IN COMPUTER HAVING PROCESSOR SYSTEM IMPLEMENTED THEREIN, AND SEMICONDUCTOR DEVICE HAVING PROCESSOR SYSTEM IMPLEMENTED THEREIN

INFORMATION DISCLOSURE STATEMENT (IDS)
UNDER § 1.97 AND § 1.98

Commissioner for Patents
Washington, D.C. 20231

Sir:

1. This IDS should be considered:

(a) when filed within three months of the filing date of the present application, or within three months of the filing date of the National Stage as set forth in § 1.491 in an international application, or before the mailing date of a first Office Action on the merits, whichever occurs last;

(b) when filed before the mailing date of either a Final Rejection under § 1.113 or a Notice of Allowance under § 1.311, whichever occurs first and when 1(a) does not apply. For this purpose, there is included herein either a certification in section 4 below (included when indicated by a marked box), or a fee of \$180.00 (a check in the amount of \$180.00 is enclosed, or if not see section 5 below);

(c) when filed prior to the payment of the Issue Fee, when 1(a)-(b) do not apply, and when a certification is included in section 4 below (included when indicated by a marked box); then the Applicant(s) hereby petition(s) and request(s) consideration of this IDS, and provided herewith is a fee of \$180.00 (a check in the amount of \$180.00 to cover the petition fee, or if not see section 5 below).

2. When 1(a)-(c) do not apply, then it is requested that this IDS be placed in the file.

3. Listing of the information submitted is on the attached Form PTO-1449, which forms a part of this IDS. A copy of each listed document is enclosed.

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4. If a fee or additional fee is required, the Commissioner is hereby authorized to charge any fee or additional fee that may be required and credit any excess to Deposit Account No. 50-1417.

5. No explanation of relevancy is being provided for the documents listed in the attached PTO-1449 Form because each is in the English language and/or discussed in the present Specification.

6. If the PTO determines that part(s) of the required content is inadvertently omitted, then it is requested that the Applicant(s) be given additional time and specific identification of such omission(s) to enable full compliance.

Respectfully submitted,


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Registration No. 30,293
Attorney for Applicants

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(703) 684-1120
Date: August 29, 2001

FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. NIT-292	SERIAL NO.
LIST OF DOCUMENTS CITED BY APPLICANT <i>(Use several sheets if necessary)</i>		APPLICANT H. FUJII et al	
		FILING DATE August 29, 2001	GROUP

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT	DATE	NAME	CLASS	SUBCLASS	FILING DATE (If Appropriate)
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						
AK						

FOREIGN PATENT DOCUMENTS

	DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	TRANSLATION NO
AL						<input type="checkbox"/>	<input type="checkbox"/>
AM						<input type="checkbox"/>	<input type="checkbox"/>
AN						<input type="checkbox"/>	<input type="checkbox"/>
AO						<input type="checkbox"/>	<input type="checkbox"/>
AP						<input type="checkbox"/>	<input type="checkbox"/>

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

AR		Welcome to the Opportunities of Binary Translation, pp. 40-45.
AS		PA-RISC to IA-64: Transparent Executioh, No Recompilation , pp. 47-52
AT		Dynamic and Transparent Binary Translation, pp. 54-59.

EXAMINER	DATE CONSIDERED
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* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE
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AL						<input type="checkbox"/>	<input type="checkbox"/>
AM						<input type="checkbox"/>	<input type="checkbox"/>
AN						<input type="checkbox"/>	<input type="checkbox"/>
AO						<input type="checkbox"/>	<input type="checkbox"/>
AP						<input type="checkbox"/>	<input type="checkbox"/>

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

AR	MICROPROCESSOR, Vol. 14, Archive 2, FEB. 20009-19	
AS	Data Speculation Support for a Chip Multiprocessor	
AT	SIMULTANEOUS MULTITHREADING, 12-19.	

EXAMINER

DATE CONSIDERED

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